



September 2013

WATS Server 4.2 Release Note



Release WATS Server 4.2

This release overview contains information about new features in WATS Server 4.2 For more information about WATS, please visit www.virinco.com/wats

Major Feature Areas

This document describes the following major feature areas of WATS 4.2:

- WATS Reporting
 - Process Capability Analysis (BETA)
 - Connection & Execution Time Report
 - Dashboard release
 - Replacing Captains View
 - New data warehouse
 - NEW Root Cause module (BETA)
- WATS Operator Interface
 - Unit Verification Report
- Miscellaneous





WATS Reporting



Test Step yield & analysis

Product by Revision yield

Process Capability Analysis Email based Yield Monitor

Product & Test yield Product yield

Total process yield

Yield

NUC.

WATS 4.2 Reporting includes the following improvements and new functionality.

TIP: To drill down and jump between reports in WATS, use the menu icon \equiv as shortcut. When using this menu, WATS will use the parameters from the selected record and auto fills the filter options.

Process Capability Analysis (BETA)

The new *Process Capability Analysis* report is designed for high volume (hundreds of thousands of units) and deep SPC analysis. The data warehouse pre-aggregates measurements down to a 1 hour resolution (meaning that if you test 100 UUTs within one hour, all measurements will be aggregated into a few calculated values).

The well-known WATS filter is present and you start the report by selecting parameters and applying the filter.

Yield Process Capability Analysis (Beta)

Product Group:	(Any)			Test O	peratio		PCBA													
Site:	(Any)		-	Test O	peradic		FCDA							-						
Site:	(Any)			From D	ate (U	TC):						0	: 00	 I 						
Part Number:	OLC-140-P;OI	.C-140-C		To Dat	e (UTC	:):					2	3 👻	: 59 -	•						
Batch Number: (range)																				
Station Name:																				
Top Count (max results):			-																	
Min Count (units):			-																	
Apply filter		- Clea	r filter	Save	filter	J														
Apply selected																				1
Part- P Number N	rodu ame Process	Tot# FP	Y# FPY	SPY	трү	Cpk 1	wo/f	#	Cpk 2	wo/f	#	Cpk 3	wo/f	#	Cpk 4	wo/f	#	Cpk 5	wo/f	#
☑	PCBA	6,262 5,4	06 86.3	94.2	96.6	0.33	0.33	0	0.33	0.36	97	0.37	0.37	0	0.50	0.50	0	0.63	0.63	0
☑	PCBA	6,157 5,3	38 86.7	96.2	98.1	0.26	0.26	300	0.23	0.52	198		1.02	33	0.02	1.29	49	0.21	1.30	17
Apply selected																				1

The first result level is a list of Part Numbers matching your filter options. Each record is matching Part Number and Process. The list display Tot# (Unique UUTs tested), FPY# (number of passed UUTs), FPY (First Pass Yield), SPY (Second Pass Yield), TPY (Third Pass Yield), CPK1 (test step with worst CPK value first), wo/F (CPK with step status = PASSED), # (number of step failures). TIP: Hold the mouse over a CPK value to see test step name.

YIELD KPI Targets: Red colour = Alarm Orange colour = Warning Green colour = Good To edit the KPI Targets, browse to *Control Panel>Configure/settings>KPI Targets*







Select one or several records and *Apply Selected*. A list with all test steps will be displayed (only test steps returning a numeric value (NLT, MNLT))

Wield Process Capability Analysis (Beta)

Apply	selected																						1
	Part- Number	Product Name	Process	Tot#	FPY#	FPY	SPY	тру	Cpk 1	wo/f	#	Cpk 2	wo/f	#	Cpk 3	wo/f	#	Cpk 4	wo/f	#	Cpk 5	wo/f	#
v =	OLC-140-P		PCBA	5,160	4,524	87.7	95.0	97.2	0.33	0.36	72	0.37	0.37	0	0.39	0.39	0	0.63	0.63	0	0.64	0.64	0
	OLC-140-C		PCBA	5,155	4,413	85.6	95.7	97.9	0.26	0.26	192	0.26	0.52	132		1.02	33	0.02	1.29	49	0.18	1.35	16
Apply	selected																						1

															1
	Step Name / Measure Name	Cpk	Ср	Cp lower	Cp upper	Cpk w/o Failed	Cp w/o Failed	Cp lower w/o Failed	Cp upper w/o Failed	Yield	Total Count	Mean	Stdev.	Low limit	Hig) limi
à à 🗆	Charge rate C31 (220µF)	0.36	0.41	0.36	0.47	1.55	1.97	1.55	2.39	98.8 %	2,192	4.93	0.69	4.2	5.9
<u>a</u> =	Charge rate C31 (220µF)	0.47	0.51	0.55	0.47	2.25	2.38	2.51	2.25	97.6 %	2,968	5.12	0.56	4.2	5.9
<u>a</u> =	Charge rate C41 (4700µF)	0.51	0.72	0.51	0.92	1.94	2.78	1.94	3.62	99.5 %	2,192	2.71	0.39	2.1	3.8
<u>a</u> <u>a</u> =	EEB Charge rate C41 (4700µF)	1.18	1.74	1.18	2.30	2.11	3.14	2.11	4.17	99.3 %	2,968	2.68	0.16	2.1	3.8
à à =	Discharge test C31	0.64	85.32	0.64	170.00	0.64	85.32	0.64	170.00	100.0 %	2,192	0.00	0.00	0	0.2
<u>a</u> <u>a</u> =	Discharge test C31	0.63	60.86	0.63	121.09	0.63	60.86	0.63	121.09	100.0 %	2,968	0.00	0.00	0	0.2
à à 🗆	Discharge test C41	0.39	0.92	0.39	1.46	0.39	0.92	0.39	1.46	100.0 %	2,192	0.04	0.04	0	0.2
<u>a</u> =	Discharge test C41	0.37	0.98	0.37	1.59	0.37	0.98	0.37	1.59	100.0 %	2,968	0.04	0.03	0	0.2
<u>a</u> =	Measure 1-10V continuity	0.33	0.36	0.40	0.33	0.36	0.39	0.41	0.36	98.3 %	4,290	2.64	2.68	-0.6	5.25
_ ⊇ ≡	Measure 1-10V continuity	0.26	0.83	1.40	0.26	23.18	69.77	116.35	23.18	99.8 %	2,859	5.05	0.25	4	5.25
	8	0.44	0.57	0.44	0.70	201.00	400.40	201.00	600 CA	00.0.0/	2.050	0.00	0.47	0.0	

The step list shows CPK calculations with and without failures, step yield, total (test) count, mean value, standard deviation, low limit and high limit. CPK:

Red colour = CPK below 1.32

Green colour = CPK between 1.33 and 4 Blue colour = CPK above 4

The left margin gives 2 "View Details" option.

- 1. Unit View. Presents data from the relation database equal to the detail report view in the *Test Step Yield & Analysis* report. Can plot every single measurement and drill down on serial numbers
- 2. Aggregated View. Presents data from the data warehouse.

More usable when large data sets. No serial number details. NOTE: You must add steps for monitoring. Click the + icon in the list to add step to the data warehouse load.

The aggregated view shows a zoom able chart where you can select grouping method and different calculation options. It will also list SPC calculations below the chart.







		w/o Failed
Unit Count	2,968	2,896
Max	11.8	5.87
Avg	5.12	5.1
Min	-0.000792	4.32
High limit	5.9	5.9
Low limit	4.2	4.2
Cpk	0.47	2.25
Ср	0.51	2.38
Cp lower	0.55	2.51
Cp upper	0.47	2.25
Stdev	0.558	0.119
Stdevp	0.558	0.119
var	0.312	0.0141
varp	0.311	0.0141
Max step time	6.73	3.23
Avg step time	2.04	2.03
Min step time	2.01	2.01
Comparison	GELE (>= AND <=)	GELE (>= AND <=)

Further, it will list distribution charts for SW filename and version, Revision and Station Name. SW Filename distribution (volume/Cpk):





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Q

Station

Station report
 OEE analysis
 GR&R analysis

Connection & Execution Time Report

To be able to see handling time between executions (typically time spent replacing the UUT in a fixture), a new Connection & Execution Time Report has been added. Browse to *Survey > Station report* and apply the filter. Select the Station (name) and click the *View report* icon. Scroll down to the bottom of the "Station Report" popup.

Conn	ection & Ex	ecution Time								
Cal 01	culated aver L m 38 s	age:	Min connection time: Max connection time:	0 h 0 r 1 h 22 r Calculate ave	m 5 s m 36 s erage					
						:	L <u>2 3 4</u>	<u>5 6 7</u>	<u>8 9 10</u>	<u> >></u>
	Serial Number	Status	PartNumb Revision	Process	Socket	Operator	UTC Start Datetime	UTC End Datetime	Connection Time	Execution Time
≡	10353045	X Failed	OLC-140-C 8	PCBA		Janis	2013-Aug-	2013-Aug-2	00 s	02 m 33 s
≡	10352764	X Failed	OLC-140-C 8	PCBA		Janis	2013-Aug-	2013-Aug-1	06 m 15 s	05 m 53 s
≡	10352764	X Failed	OLC-140-C 8	PCBA		Janis	2013-Aug-:	2013-Aug-2	15 m 36 s	01 m 35 s
≣	10306002	🗸 Passed	OLC-140-C 8	PCBA		Janis	2013-Aug-	2013-Aug-2	01 m 01 s	02 m 18 s
≡	10353045	🗸 Passed	OLC-140-C 8	PCBA		Janis	2013-Aug-	2013-Aug-2	54 s	02 m 22 s
≣	10352764	X Failed	OLC-140-C 8	PCBA		Janis	2013-Aug-3	2013-Aug-2	47 s	01 m 38 s
≡	10306002	X Failed	OLC-140-C 8	PCBA		Janis	2013-Aug-:	2013-Aug-2	08 m 27 s	07 m 31 s
≣	10306002	🗸 Passed	OLC-140-C 8	PCBA		Janis	2013-Aug-3	2013-Aug-2	19 s	02 m 25 s
≡	10306002	🗸 Passed	OLC-140-C 8	PCBA		Janis	2013-Aug-:	2013-Aug-2	46 s	03 m 22 s
≣	10306002	X Failed	OLC-140-C 8	PCBA		Janis	2013-Aug-	2013-Aug-2	24 s	03 m 08 s
≡	10306002	🗸 Passed	OLC-140-C 8	PCBA		Janis	2013-Aug-:	2013-Aug-2	05 m 15 s	02 m 16 s
≡	10353045	X Failed	OLC-140-C 8	PCBA		Janis	2013-Aug-	2013-Aug-2	01 m 57 s	02 m 10 s
≡	10353045	🗸 Passed	OLC-140-C 8	PCBA		Janis	2013-Aug-	2013-Aug-2	06 m 12 s	02 m 30 s
≣	10352764	X Failed	OLC-140-C 8	PCBA		Janis	2013-Aug-	2013-Aug-2	03 m 50 s	01 m 40 s
≡	10352764	X Terminated	OLC-140-C 8	PCBA		Janis	2013-Aug-	2013-Aug-2	18 m 14 s	02 m 09 s
≡	12450944	🗸 Passed	OLC-140-C 8	PCBA		Janis	2013-Aug-	2013-Aug-2	17 h 02 m	02 m 29 s
=	12450945	🗙 Failed	OLC-140-C 8	PCBA		Janis	2013-Aua-:	2013-Aug-2	04 m 09 s	02 m 06 s

The average Connection Time is calculated in the header of the report. By default, the minimum and maximum connection time available in the table below is use for calculation. You can modify these values and recalculate the average time.

Dashboard

The dashboard module is now released. You can easily create customizable dashboards with refresh capabilities and custom filters. Each user can create their own private dashboards, or a super-user can create and share public dashboards.









You may select among several different charts/tables and combine them with your own WATS filters. With this flexibility, you can arrange and set up dashboards that suite your needs.

💽 🛛 📔 Exit edit mode 🎫			
Mark widgets to add			
	eports Rej	pair reports 🧧 Unit s	tatisti
🚊 📇 Charts	0	0 🚪 Today	
Test Volume	540	0 🗧 Last 7	days:
	804	0 📒 Currer	nt mont
E Repair Volume	2546	0 Currer	nt year:
Yield	52128	328 Total:	
Product Yield			
Failure Type			
Total Process Yield			
Process Overview (MES)			
Phase Overview (MES)			
Unit Throughput (MES)			
🖃 🗁 Tables			
Test Statistics			
Unit Statistics			
Volume Yield CPK			
Worst Yield			
Top Repair			
🖃 😋 Misc	ne – last 24 r	iours	
Web Viewer			
Add to : 💿 1.Section 🔻 😳 2.Section 👻			



Replacing Captains View

Public Dashboards

ili 💿	Overview
duigo	WATS Up - last 24 hours
duigo	WATS Up - last 7 days
duigo	WATS Up - last 30 days

The Dashboard Module will replace the Captains View reporting tool. In the Public Dashboards area, 3 predefined dashboards are listed (WATS UP).

Volume Yield CPK - Last 30 days

-	_																		_	_		-
Part-Number	Product Name	Process	Tot#	FPY#	FPY	SPY	TPY	Cpk 1	wo/f	#	Cpk 2	wo/f	#	Cpk 3	wo/f	. #	Cpk 4	wo/f		Cpk 5	wo/f	
DLC-140-P		PCBA	330	307	93.0 %	97.3 %	98.8 %	0.30	0.30	0	0.70	0.70	0	0.69	3.03	2	1.35	4.47	4	35.92	35.92	0
LC-140-C		PCBA	316	239	75.6 %	92.4 %	93.4 %	1.03	1.15	6	0.16	1.21	6	-0.01	1.71	з	0.01	2.05	2	-0.02	2.15	2
LC-140		Product Inte	89	89	100.0 9	100.0 9	100.0 9	?														
																						1
arst Vield - L	ast 30 day	15									Worst De	pair – Las	+ 30 da	we								
orst Yield – Li	ast 30 day	/5									Worst Re	pair - Las	t 30 da	iys								
orst Yield – Li	ast 30 day	/5							1	^	Worst Re	pair - Las	t 30 da	lys								1
orst Yield – Li	ast 30 day	/S Process	Total	FPY Cou	unt	FPY	s	рү	1 ТРҮ	^	Worst Re	pair – Las Product	t 30 da Tota	Default	Compos	Desi	nn Mar	nual Ar	utoma	No	Replace	1 Scrappe
art Name Name Name Name Name Name Name Name	ast 30 day roduct ame	Process PCBA	Total Count 316	FPY Cou	unt 239 7	FPY 75.6 %	S 92.4	PY 9% 1	1 TPY 93.4 %	^	Worst Re	pair – Las Product Name	t 30 da Tota Count	Default	Compor	n Desi	gn Mar Pro	nual Au cess Pr	utoma ⁱ rocess	No Failure Found	Replace	1 Scrappe
art Pr Jumber Na JLC-140-C	ast 30 day roduct ame	Process PCBA PCBA	Total Count 316 330	FPY Cou 2 3	unt 139 7 107 S	FPY 75.6 % 93.0 %	S 92.4 97.3	PY %	1 TPY 93.4 % 98.8 %	^	Worst Re Part Number	pair – Las Product Name	t 30 da Tota Count	uys Default	Compor	n Desi	gn Mar Pro	nual Ar cess Pr	utoma [;] rocess	No Failure Found	Replace	1 Scrappe
art Pr umber Na LC-140-C LC-140-P LC-140	ast 30 day roduct ame	/S PCBA PCBA Product Inte	Total Count 316 330 89	FPY Cou 2 3	unt 339 7 307 9 89 1	FPY 75.6 % 03.0 % 00.0 %	S 92.4 97.3 100.0	PY % 1 % 1	1 TPY 93.4 % 98.8 % .00.0 %	^	Worst Re	pair – Las Product Name	t 30 da Tota Count	Default	Compor	Desi	gn Man Pro ds retur	nual Ar cess Pr ned.	utoma ⁱ rocess	No Failure Found	Replace	1 Scrappe
art Pr Number N2 DLC-140-C DLC-140	ast 30 day roduct ame	Process PCBA PCBA Product Inte	Total Count 316 330 89	FPY Cou 2 3	unt 239 7 307 9 89 1	FPY 75.6 % 03.0 %	S 92.4 97.3 100.0	PY 9% 1	1 TPY 93.4 % 98.8 % .00.0 % 1	^	Worst Re Part Number	Product Name	t 30 da Tota Count	Default	Compor	Desi	gn Mar Pro	nual An Cess Pr ned.	utoma ^r rocess	No Failure Found	Replace	1 Scrappe

The default view is similar to Captains View. The top report is identical with the *Process Capability Analysis* report. By clicking on one of the CPK values, it will automatically open the *Process Capability Analysis* report and highlight the selected step. You can also click on the menu icon in the top left corner and select "open as report" for full filter options.

Apply	selected																			1
	Part- Number	Product Name	Process	Tot#	FPY#	FPY	SPY	тру	Cpk 1	wo/f	# ^C	2 wo	/f #	Cpk 3	wo/f	# Cpk	wo/f	#	Cpk 5 w	o/f #
☑ =	OLC-140-P		PCBA	330	307	93.0 %	97.3	98.8	0.30	0.30	o o.	70 0.7	0 0	0.69	3.03	1.35	4.47	4 3	5.92 35	92 0
Apply	selected																			1
																				1
_																				-
													Ср	Cr						
	Step Name	e / Measu	re Name			c	Cpk	Ср	Cp lower	Cp upper	Cpk w/o Failed	Cp w/o Failed	Cp lower w/o Failer	Cp uppe w/o	Yie	d Tota Coun	l Mean	Stde	ev. Lou	r High t limit
àà	Step Name	e / Measu	re Name 220µF)			0	Cpk .69	Ср 0.71	Cp lower	Cp upper	Cpk w/o Failed	Cp w/o Failed	Cp lower w/o Failed 3.09	Cp uppe w/o Faile 3.03	Yie 99.4	d Tota Court	Mean 5.07	Stde	ev. Lov lim	High Himit
<u>a</u> <u>a</u> <u>a</u>	Step Name	e / Measu rate C31 (: rate C41 (:	re Name 220µF) 4700µF)			0	.69	Ср 0.71 2.18	Cp lower 0.73 1.35	Cp upper 0.69 3.02	Cpk w/o Failed 3.03 4.47	Cp w/o Failed 3.06 7.16	Cp lower w/o Failed 3.09 4.47	Cp uppe w/o Faile 3.03 9.84	99,4 98.8	d Tota Court 6 330	Mean 5.07 2.63	Stde 0.40	ev. Lov lim 0 4.3 3 2.3	High limit 5.9 3.8
2 2 2 2 2	Step Name	e / Measu rate C31 (; rate C41 (; rge test C3)	re Name 220µF) 4700µF)			0 1 0	Cpk .69 1.35	Cp 0.71 2.18 54.46	Cp lower 0.73 1.35 0.70	Cp upper 0.69 3.02 108.22	Cpk w/o Failed 3.03 4.47 0.70	Cp w/o Failed 3.06 7.16 54.46	Cp lower w/o Failed 3.09 4.47 0.70	Cp uppe w/o Faile 3.03 9.84 108.2	99.4 98.8 100.0	d Tota Cour 6 330 6 330 6 330	k Mean 5.07 2.63 0.00	Stde 0.40 0.13 0.00	2 v. Lov lim 0 4.3 3 2.3 0 0	High limit 5.9 3.8 0.2
2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	Step Name E23 Charge E23 Charge E23 Dischar E23 Dischar E23 Dischar	e / Measu rate C31 (; rate C41 (; rge test C31 rge test C41	re Name 220µF) 4700µF) 1			0	Cpk .69 1.35 .70 .30	Cp 0.71 2.18 54.46 1.67	Cp lower 0.73 1.35 0.70 0.30	Cp upper 0.69 3.02 108.22 3.05	Cpk w/o Failed 3.03 4.47 0.70 0.30	Cp w/o Failed 3.06 7.16 54.46 1.67	Cp lower w/o Failed 3.09 4.47 0.70 0.30	Cp uppe w/c 3.03 9.84 108.2 3.05	99.4 998.8 2 100.0	d Tota Coun 6 330 6 330 6 330 6 330	Hean 5.07 2.63 0.00 0.02	Stde 0.40 0.12 0.00	2 Contraction (High Imit 5.9 3.8 0.2 0.2

Use the menu icon \equiv in the top left corner for the other 2 reports for drill down.





NEW Root Cause module (BETA)



The Root Cause Analysis (RCA) module is integrated into the WATS Reporting tool. It is a general tool following the D8 RCA setup and can be used for any cases, not only WATS related. To create a ticket (a case is called a ticket), either click on the + New ticket icon in the Root Cause main menu, or use the action menu icon from one of the reports (Create RCA ticket). By using the action menu, WATS will use the parameters in the selected record as searchable tags and if created from the UUT or UUR report, add a link to the "Referenced UUT/UUR report".

Ticke	ts Mytickets	New ticket	
	All tickets	🖺 Subject	
	Unsolved tickets	Why keeps this unit failing the same test step?	×
	Unassigned tickets	🐠 Tags	Priority
	Closed tickets	12450883 × OLC-140-C × 8 × LUMINYX-TESTSTA × PCBA ×	Normal -
+	New ticket	Referenced UUT Report	

Further start the D8 process with defining a team (D1) - ticket creator and assigned user will be added by default - and describe the problem (D2). Either directly in the text box or by opening the HTML editor with image upload support. Other files can also be uploaded using the "Attach File" option below the text box.

D1: Define Team

Add e-mail addresses (separate with semicolon). Receipients will receive an e-mail notification when the ticket is created or updated.

D2:	Describe	the	Prob	lem

HTML Attach File

Assigned to

Add comment

The unit has failed the sai	me test step several times. Why?

rep@virinco.com × tal@virinco.com × te@virinco.com ×

Assign the ticket to a WATS User and click Submit as New

wn				
Tom Andres Lomsdalen	-	Ν	Submit as New	-

The ticket will now be available in the "My Tickets" list for the creator and the assignee.





WATS Server 4.2 Release Note

	OLC-140-C ×	LUMINYX-TESTSTA ×	Priority:	Normal	•	Referenced Report
D1: Define Team						
D2: Describe the	Problem					
03: Implement Im	mediate Actior	15				
Add comment						
Looks like the label w	vas misplaced at	the bottom of the PCB bord. Rep	blaced label.			
HTML Attach Fil	e					
Richard Pettersen wro	te Sep 19 2013 1	3:02 (UTC)	Penlaced label			
Richard Pettersen wro ooks like the label	te Sep 19 2013 1 was misplaced a	3:02 (UTC) at the bottom of the PCB bord. I	Replaced label.			
Richard Pettersen wro ooks like the label D4: Perform Root	te Sep 19 2013 1 was misplaced a Cause Analysis	3:02 (UTC) It the bottom of the PCB bord. I	Replaced label.			
Richard Pettersen wro Looks like the label D4: Perform Root (D5: Implement Co	te Sep 19 2013 1 was misplaced a Cause Analysis rrective Action	3:02 (UTC) It the bottom of the PCB bord. I ; ;	Replaced label.			
Richard Pettersen wro Looks like the label D4: Perform Root D5: Implement Co D6: Confirm Action	te Sep 19 2013 1 was misplaced a Cause Analysis rrective Action n Effect	3:02 (UTC) It the bottom of the PCB bord. I S	Replaced label.			
Richard Pettersen wro Looks like the label D4: Perform Root (D5: Implement Co D6: Confirm Action D7: Implement Pre	te Sep 19 2013 1 was misplaced a Cause Analysis rrective Action n Effect eventive Action	3:02 (UTC) It the bottom of the PCB bord. I is IS	Replaced label.			
Richard Pettersen wro Looks like the label D4: Perform Root D5: Implement Co D6: Confirm Action D7: Implement Pre D8: Approve and C	te Sep 19 2013 1 was misplaced a Cause Analysis rrective Action n Effect eventive Action Close	3:02 (UTC) It the bottom of the PCB bord. I is IS	Replaced label.			
Richard Pettersen wro Looks like the label D4: Perform Root (D5: Implement Co D6: Confirm Action D7: Implement Pre D8: Approve and C	te Sep 19 2013 1 was misplaced a Cause Analysis rrective Action n Effect eventive Action Close	3:02 (UTC) It the bottom of the PCB bord. I is IS	Replaced label.			
Richard Pettersen wro Looks like the label D4: Perform Root (D5: Implement Co D6: Confirm Action D7: Implement Pre D8: Approve and C	te Sep 19 2013 1 was misplaced a Cause Analysis rrective Action n Effect eventive Action Close	3:02 (UTC) It the bottom of the PCB bord. I is is	Replaced label.			

The module now allows team members to follow through the complete RCA analysis and Solve/Close tickets. For details, please visit our Resource Center at http://support.virinco.com





WATS Operator Interface



Unit Verification Report

This report verifies whether a unit has been correctly processed or not, based on its current history against a set of rules defined in Product Test Operation (Control Panel), to determine its status and grade.

The status gives a high level indication if the unit has succeeded through its processes or not.

- **OK** All tests are passed.
- **Warning** All tests are passed, but not in order of process index (process index is configured in WATS CP).
- **NOT OK** If one or more of the listed reports are not passed.

The grade gives a more detailed explanation to why the unit has the given status

- **A** All processes have passed their tests, and they have been executed in correct order.
- **B** All processes have passed their tests, but they have not been executed in correct order.
- **C** All processes have passed their last test, one or earlier run failed, but unit has not been repaired. <u>No Failure Found</u> <u>does not count as repaired.</u>
- D All processes have passed their last test, but the unit has been repaired. <u>No Failure Found does not count as</u> <u>repaired.</u>
- N/A Some of the processes have not passed their last test.









WATS Control Panel



Product Test Operation (Last test operation)

Product Test Operation is a rename of "Last Test Operation". The specifications is used for the "Total process yield" and "Unit Verification" reports.

E.g. The "Unit Verification" report will check if a scanned unit has completed and PASSED all Test Operations (Process) specified. The "Total process yield" will use the Test Operation (Process) with highest Index to calculate Yield.

Product test operation can be specified for a Product Group, Part Number or Part Number and Revision, then select one or several Test Operations applicable for the selection

Miscellaneous

- UUT report. Execution time is now formatted (DD.HH.MM.SS) and presented in addition to seconds.
- Operator Interface. When scanning a Serial Number that matches multiple Part Numbers, a dialog pop up allow the operator to select correct Part Number.
- Software Module.
 - o GUI enhancements to the Software Manager
 - Added API functionality
- Miscellaneous query and database enhancements

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